Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
Li	18734	ddr or (double adj5 data adj5 rate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 12:57
L2	45	L1 same multiplexer\$3 same (switch\$2 or transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 12:57
L3	2637	(sense adj3 amplifier\$2) same amplifier\$2 same multiplexer\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 12:58
L4	793	L3 same (switch\$2 or transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 12:58
L5	256	L4 same (register or flip\$1flop\$3 or latch\$2 or delay)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 12:58
L6	23	1 and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 12:58
L7	6207	(sense adj3 amplifier\$2) and amplifier\$2 and multiplexer\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 12:59
L8	4636	L7 and (switch\$2 or transistor) and (register or flip\$1flop\$2 or latch\$2 or delay)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 12:59

L9	381	L8 and L1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 12:59
L10	25	L9 and (output adj3 driver\$2) and pipe\$1lin\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 12:59
L11	8	10 and 365/189.01,189.02,189. 04,189.058,189.12,194.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 13:02
L12	15	10 and "365"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 12:59
L13	261252	non\$1volatil\$2 or (flash\$2 adj3 memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 13:00
L14	10397	L13 near5 (memory adj3 array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 13:00
L15	44	L14 same trigger\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 13:00
L17	1062	L14 same controller\$2 same (memory adj4 array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 13:01

L18	1505	L13 same controller\$2 same (memory adj3 array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 13:01
L19	24	L18 same ECC same logic\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 13:01
L20	65	17 and 18 and 365/189.01,189. 02,189.04,189.058,189.12,194. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 13:02
L21	14	(("6763424") or ("6145051") or ("6567307") or ("6772274") or ("6678785") or ("5937425") or ("5404485")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/18 13:03
L22	28	(("4694454") or ("5263032") or ("6199139") or ("5475693") or ("6560152") or ("5963473") or ("5699297") or ("6415325") or ("6151246") or ("5835413") or ("5652720") or ("6345001") or ("5270979") or ("5504760")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/18 13:04
L23	6	(("4694454") or ("5263032") or ("6199139")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/18 13:05
L24	18	Gonzalez-carlos.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 13:05
L25	1	conley-kevin.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 13:05

L26	0	(method and (flash adj memory) and sub\$1array and block\$1 and stor\$3 and data and (scrub\$3 adj trigger\$2) and read\$3 and correct\$3 and error\$2 and (write or writt\$3) and un\$1correct\$3). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 13:12
L28	0	(method and (flash adj memory) and (scrub\$2 adj trigger\$2) and stor\$3 and array and (read or read\$3) and temporarily and (program or programm\$3) and error\$2 and correct\$3 and (writ or write or writt\$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 13:14
L29	0	(method and re\$1programmabl\$3 and non\$1volatile and memory and charg\$2 and data and error\$2 and (read or read\$3) and re\$1cover).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/18 13:16